

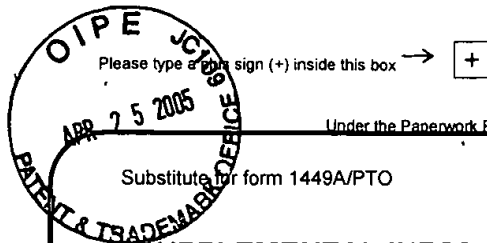
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Respectfully submitted,



Keith M. Tackett
Registration No. 32,008
MOSER, PATTERSON & SHERIDAN, L.L.P.
3040 Post Oak Blvd. Suite 1500
Houston, TX 77056
Telephone: (713) 623-4844
Facsimile: (713) 623-4846
Attorney for Applicant(s)



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Application Number

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Filing Date

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First Named Inventor

Ma, et al.

Group Art Unit

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Examiner Name

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Submission Date

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (if known)			
	A1	US-6451119 B2	09/17/2002	SNEH, ET AL.	
	A2	US-6335280 B1	01/01/2002	VAN DER JEUGD	
	A3	US-6291319 B1	09/18/2001	YU, ET AL.	
	A4	US-20050079691 A1	04/14/2005	KIM, ET AL.	
	A5	US-20030189208 A1	10/09/2003	LAW, ET AL.	
	A6	US-20020145168 A1	10/10/2002	BOJARCZUK, JR., ET AL.	
	A7				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)				
	B1					

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
	C1	International Search Report mailed February 22, 2005 for PCT/US2004/030872 (AMAT/8539-PCT)	
	C2	Kamins, et al. "Kinetics of selective epitaxial deposition of Si _{1-x} Ge _x ", Applied Physics Letters, American Institute of Physics. New York, US, vol. 61, no. 6, 10 August 1992, pp. 669-671	
	C3	Menon, et al. "Loading effect in SiGe layers grown by dichlorosilane- and silane-based epitaxy", Journal of Applied Physics, American Institute of Physics. New York, US, vol. 90, no. 9, 1 November 2001, pp. 4805-4809	
	C4	Sedgwick, et al. "Selective SiGe and heavily As doped Si deposited at low temperature by atmospheric pressure chemical vapor deposition", Journal of Vacuum Science and Technology: Part B, American Institute of Physics. New York, US, vol. 11, no. 3, 1 May 1993, pp. 1124-1128	
	C5	Uchino, et al. "A Raised Source/Drain Technology Using In-situ P-doped SiGe and B-doped Si for 0.1 µm CMOS ULSIs", Electron Devices Meeting, 1997. Technical Digest, International Washington, DC, USA 7-10 Dec. 1991, New York, NY, USA, IEEE, US, 7 December 1997, pp. 479-482	

Examiner

Date Considered

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